

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A switch system comprising:
a radio frequency switch element configured to receive a radio frequency (RF) input signal;
a first voltage supply terminal configured to receive a first DC supply voltage;
a boost circuit configured to receive the RF input signal and the first DC supply voltage and in response, provide a bias DC output voltage for controlling that allows the RF input signal to be routed through the switch element, wherein the DC output voltage is greater than the first DC supply voltage.

2. (Currently Amended) The switch system of claim 1, wherein the boost circuit comprises:
a first capacitor having a first terminal coupled to receive the RF input signal; and
a rectifier circuit coupled to a second terminal of the capacitor and the first voltage supply terminal, wherein the rectifier circuit is configured to provide the bias DC output voltage in response to the RF input signal and the first DC supply voltage.

3. (Original) The switch system of claim 2, wherein the rectifier circuit comprises a first diode and a first resistor coupled in series between the first voltage supply terminal and the second terminal of the first capacitor.

4. (Original) The switch system of claim 3, wherein the rectifier circuit further comprises a second diode and a second resistor coupled in series between the second terminal of the first capacitor and the switch element.

5. (Original) The switch system of claim 4, further comprising a second capacitor coupled between the first voltage supply terminal and the switch element.

6. (Original) The switch system of claim 5, wherein the second capacitor and the series-connected second diode and second resistor are coupled at a first node, and further comprising a third resistor coupled between the first node and the switch element.

7. (Original) The switch system of claim 4, further comprising a second capacitor coupled between a second voltage supply terminal and the switch element.

8. (Original) The switch system of claim 7, wherein the second capacitor and the series-connected second diode and second resistor are coupled at a first node, and further comprising a third resistor coupled between the first node and the switch element.

9. (Original) The switch system of claim 4, further comprising a second capacitor and a third resistor connected in parallel between the second diode and the switch element.

10. (Original) The switch system of claim 4, further comprising a third resistor coupled in series with the second resistor and the second diode.

11. (Currently Amended) The switch system of claim 3, wherein a cathode of the first diode ~~element~~ is coupled to the second terminal of the first capacitor.

12. (Currently Amended) The switch system of claim 3, wherein an anode of the first diode ~~element~~ is coupled to the second terminal of the first capacitor.

13. (Currently Amended) The switch system of claim 4, wherein a cathode of the first diode ~~element~~ and an anode of the second diode ~~element~~ are coupled to the second terminal of the first capacitor.

14. (Currently Amended) The switch system of claim 3, wherein an anode of the first diode ~~element~~ and a cathode of the second diode ~~element~~ are coupled to the second terminal of the first capacitor.

15. (Original) The switch system of claim 1, wherein the first supply voltage is ground.

16. (Currently Amended) A method of controlling a radio frequency (RF) switch comprising:

applying an RF input signal to the RF switch and a boost circuit;

applying a first DC supply voltage to the boost circuit;

generating a ~~bias~~ DC output voltage in response to the first DC supply voltage and the RF input signal, wherein the DC output voltage is greater than the first DC supply voltage; and

controlling the RF switch element with the ~~bias~~ DC output voltage, wherein the DC output voltage allows the RF input signal to be routed through the RF switch.

17. (Currently Amended) The method of claim 16, wherein the step of generating the ~~bias~~ DC output voltage comprises charging and discharging a first capacitor in response to the first DC supply voltage and the RF input signal.

18. (Currently Amended) The method of claim 17, wherein the step of generating the ~~bias~~ DC output voltage further comprises charging and discharging a second capacitor in response to the first DC supply voltage and the RF input signal.

19. (Currently Amended) The method of claim 17, wherein the step of generating the ~~bias~~ DC output voltage further comprises charging and discharging a second capacitor in response to a second DC supply voltage and the RF input signal.

20. (Canceled)

21. (New) A switch system comprising:
a radio frequency switch element configured to receive a radio frequency (RF) input signal and a first DC supply voltage;
a first voltage supply terminal configured to receive the first DC supply voltage;
a boost circuit configured to receive the RF input signal and the first DC supply voltage and in response, provide a DC output voltage for controlling the switch element, wherein the DC output voltage is greater than the first DC supply voltage.